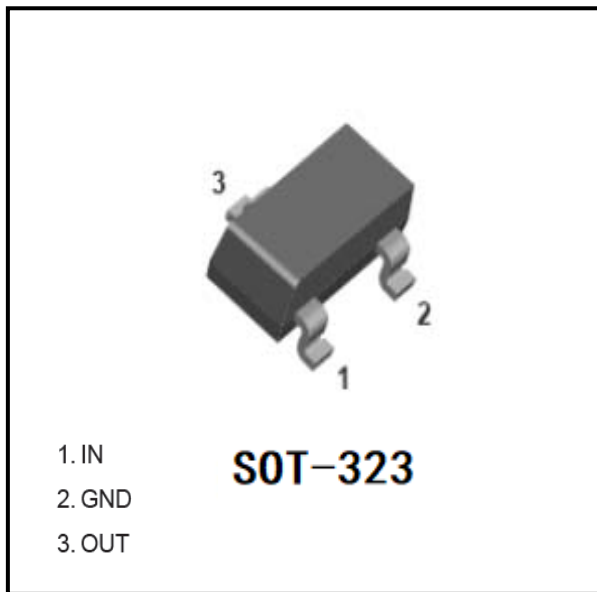


NPN Digital Transistors (Built-in Resistors)



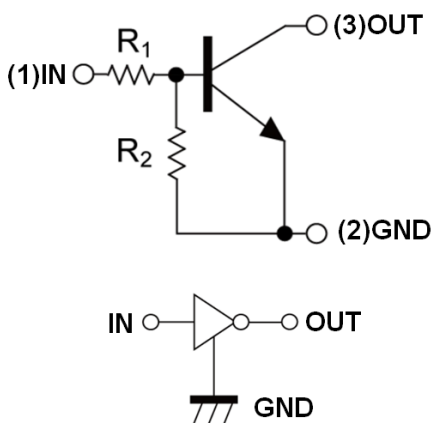
Features

- Epoxy meets UL-94 V-0 flammability rating
- Moisture Sensitivity Level 1
- Built-in bias resistors enable the configuration of an inverter circuit without connecting external input resistors
- Surface mount package ideally Suited for Automatic Insertion

Mechanical Data

- **Package:** SOT-323
- **Terminals:** Tin plated leads, solderable per J-STD-002 and JESD22-B102
- **Polarity:** Cathode line denotes the cathode end
- **Marking:** 84

Equivalent circuit



Maximum Ratings (Ta=25°C Unless otherwise specified)

Item	Symbol	Unit	Conditions	Value
Supply voltage	V_{CC}	V		50
Input voltage	V_{IN}	V		-10 to +30
Output current	I_o	mA		100
Power dissipation	P_D	mW		200
Junction temperature	T_J	°C		-55 to +150
Storage temperature	T_{STG}	°C		-55 to +150



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■ Electrical Characteristics (Ta=25°C unless otherwise specified)

Item	Symbol	Unit	Conditions	Min	Typ	Max
Input voltage	$V_{I(off)}$	V	$V_{CC}=5V, I_O=100\mu A$	0.8		
	$V_{I(on)}$	V	$V_O=0.3V, I_O=2mA$			3.0
Output voltage	$V_{O(on)}$	V	$I_O/I_I=10mA/0.5mA$			0.3
Input current	I_I	mA	$V_I=5V$			0.88
Output current	$I_{O(off)}$	uA	$V_{CC}=50V, V_I=0$			0.5
DC current gain	G_I		$V_O=5V, I_O=10mA$	24		
Input resistance	R_1	k Ω		7	10	13
Resistance ratio	R_2/R_1			0.37	0.47	0.57
Transition frequency	f_T	MHz	$V_O=10V, I_O=5mA, f=100MHz$		250	

■ Ordering Information (Example)

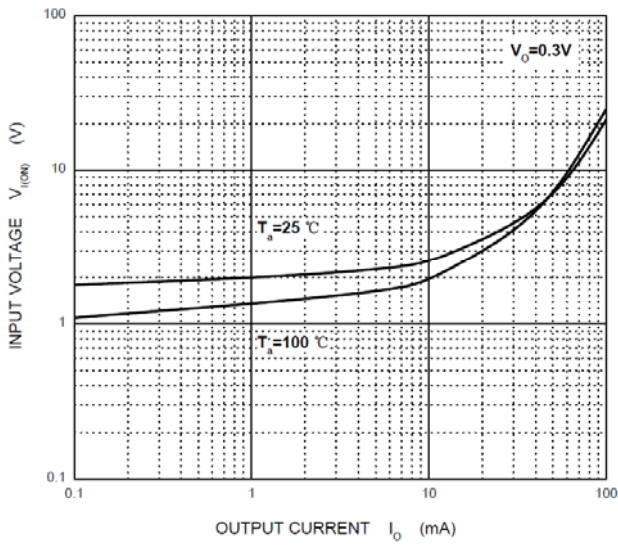
Preferred P/N	Packing Code	Unit Weight(G)	Minimum Package(Pcs)	Inner Box Quantity(Pcs)	Outer Carton Quantity(Pcs)	Delivery Mode
DTC114WUA	F2	Approximate 0.005	3000	30000	120000	7" reel



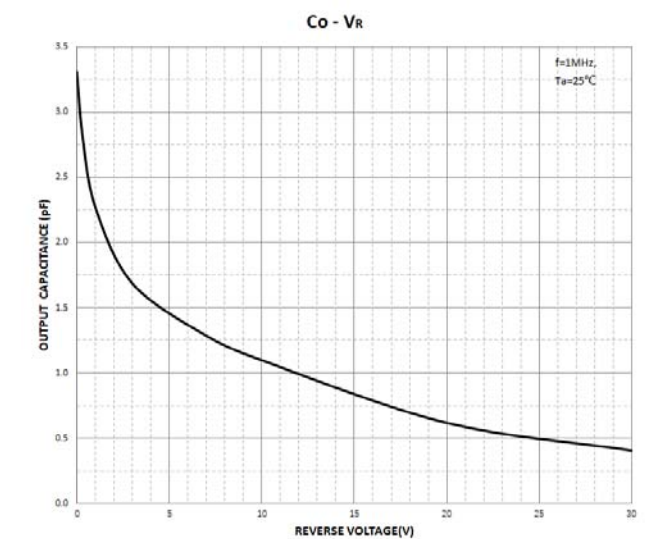
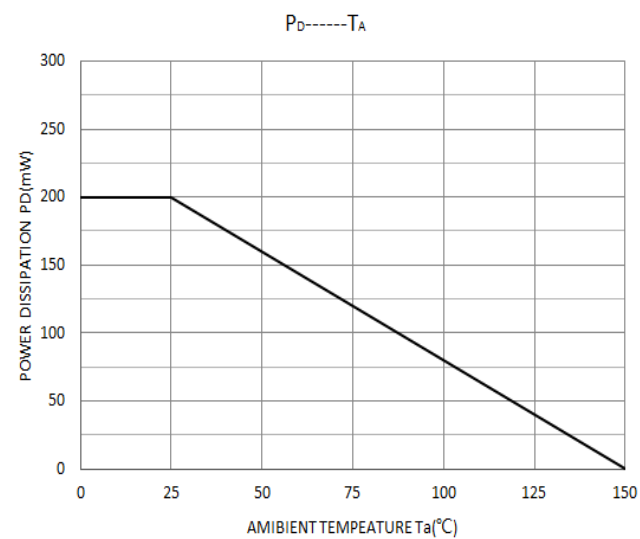
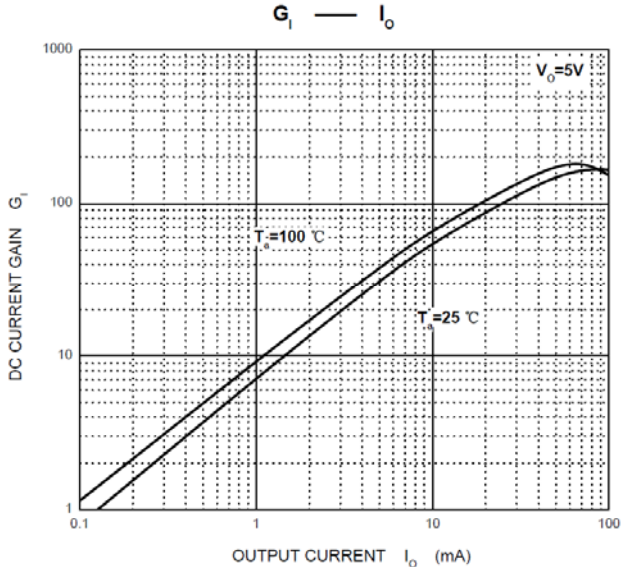
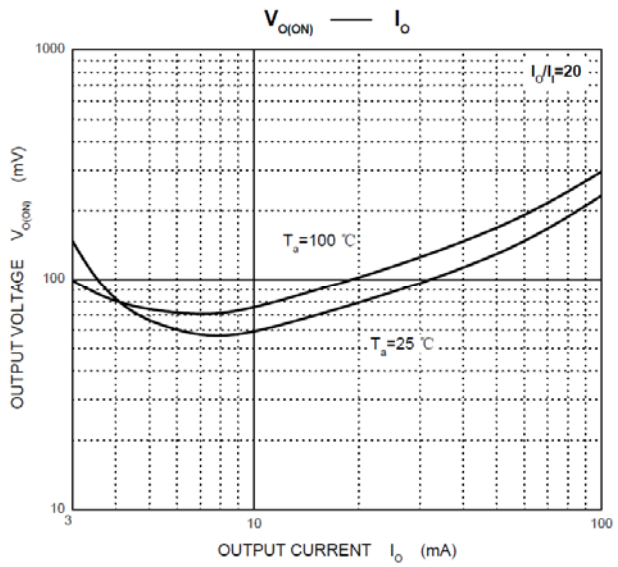
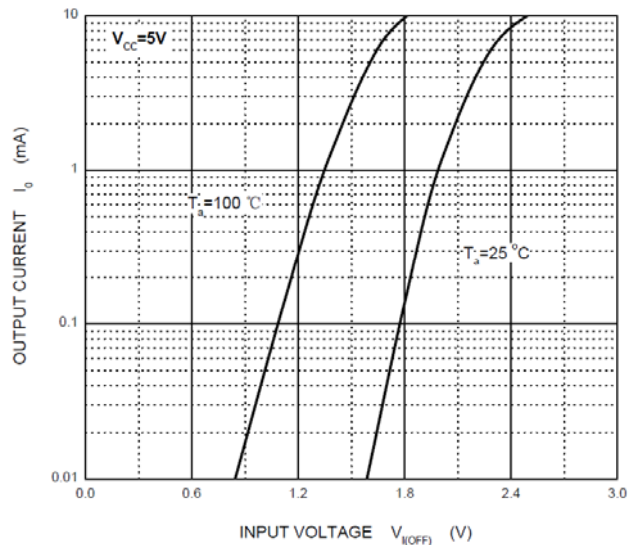
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Characteristics (Typical)

ON Characteristics

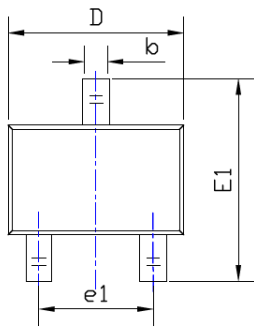


OFF Characteristics

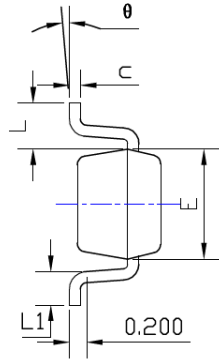




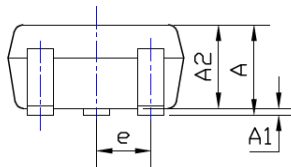
■ SOT-323 Package Outline Dimensions



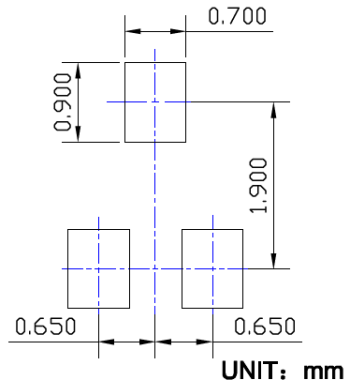
TOP VIEW



SIDE VIEW



SIDE VIEW



SUGGESTED SOLDER PAD LAYOUT

SYMBOL	DIMENSIONS			
	INCHES		Millimeter	
	MIN.	MAX.	MIN.	MAX.
A	0.035	0.043	0.900	1.100
A1	0.000	0.004	0.000	0.100
A2	0.035	0.039	0.900	1.000
b	0.006	0.016	0.150	0.400
c	0.004	0.010	0.100	0.250
D	0.071	0.087	1.800	2.200
E	0.045	0.053	1.150	1.350
E1	0.085	0.096	2.150	2.450
e	0.026 TYP		0.650 TYP	
e1	0.047	0.055	1.200	1.400
L	0.021 REF		0.525 REF	
L1	0.010	0.018	0.260	0.460
theta	0°	8°	0°	8°

NOTE:

1. PACKAGE BODY SIZES EXCLUDE MOLD FLASH AND GATE BURRS.

2. TOLERANCE 0.1mm UNLESS OTHERWISE SPECIFIED.

3. THE PAD LAYOUT IS FOR REFERENCE PURPOSES ONLY.

Note:

- All dimensions are in millimeters (mm) unless otherwise specified.
[所有尺寸均以毫米为单位，除非另有说明]
- General tolerances: $\pm 0.10\text{mm}$ unless otherwise specified.
[通用公差为 $\pm 0.10\text{mm}$ ，除非另有说明]
- Dimensions and tolerances per ASME Y14.5M-2018.
[尺寸和公差遵循 ASME Y14.5M-2018 标准]
- All dimensions shown are exclusive of burrs and gate residues.
Burrs and gate vestiges shall not exceed 0.15 mm in maximum.
[所有尺寸均不包括毛刺和浇口残留。毛刺与浇口残留的尺寸最大不得超过 0.15mm]
- Dimension b does not include dambar protrusion of max 0.100 mm per side.
[尺寸b不包括单边最大0.100 MM的中筋凸出部分]
- Dimensions D and E are the overall extreme outer dimensions of the mold compound. These dimensions exclude mold flash, lead flash, protrusions and burrs but include the maximum allowable mold mismatch.
[D和E是塑封体的外部极限尺寸，不包括包封溢料、内引线溢料、凸出部分以及胶体毛刺，但是包含了包封错位的最大尺寸]
- Formed leads shall be planar with respect to one another within a maximum of 0.076 mm relative to the seating plane.
[成型的管脚应为同一平面，共面性最大为0.1mm]
- ★It is the key size.
[★ 标记为关键尺寸]



DTC114WUA

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